

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**INFORMATION DISCLOSURE STATEMENT**

APPLICANT(S): Kerry Berland                      DOCKET NO.: 09771110-0010  
SERIAL NO.: 10/821,551                      GROUP ART UNIT: 2862  
DATE FILED:              April 8, 2004                      EXAMINER: Jay M. Patidar  
INVENTION: MULTI-TEST CIRCUIT INTERRUPTER TESTER

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

S I R:

In accordance with the provisions of 37 C.F.R. § 1.56, Applicants request that citation and examination of the references identified on the attached PTO-SB08A form, in accordance with 37 C.F.R. §1.98, be made during the course of examination of the above-referenced application for United States Letters Patent. The references listed on the attached form PTO/SB/08a relate to circuit breaker tester technology. A copy of the non-patent literature documents are attached.

Respectfully submitted by,

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